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A Ph.D. physicist with 2–5 years experience is required to manage an applied research department doing work on semi-conductors, dielectrics and the mechanical properties of pure materials. This work is closely integrated into a strong corporate research effort and is part of the well established Research and Development Division. Solid state experience would be most helpful in the assumption of broad responsibility for this group. Send full particulars to:

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edge regarding the physics of highly ionized gases of natural or laboratory origin—including their creation, containment, heating, and acceleration; their radiation, oscillations, and stability; and their transport, collective, and wave properties".

The technical program of the meeting will include invited papers on subjects of current interest in these areas, as well as ten-minute papers contributed by members of the APS. In addition, plans have been made to reserve Saturday, November 5, for a guided tour of the Oak Ridge National Laboratory.

Abstracts of contributed papers, prepared in accordance with the rules and instructions set forth in any recent issue of the *Bulletin of the American Physical Society*, should be submitted before September 26 to Dr. Albert Simon, Oak Ridge National Laboratory, P. O. Box X, Oak Ridge, Tenn. Further information concerning the meeting can be obtained from Dr. A. H. Snell at the same address.

Solid-State Circuits

CIRCUIT properties and design techniques related to a wide variety of solid-state devices will be discussed at the 1961 International Solid-State Circuits Conference, which is to be held February 15–17, 1961, on the campus of the University of Pennsylvania and at the Sheraton Hotel in Philadelphia. The conference will be sponsored jointly by the Institute of Radio Engineers, the American Institute of Electrical Engineers, and the University of Pennsylvania.

Among the general areas to be covered by the program are (1) solid-state memory, storage, and logic elements (such as twistors, thin-film memories and associated circuits, photoelectronic circuitry, etc.); (2) solid-state microwave amplifying mechanisms (such as parametric amplifiers and masers); (3) solid-state devices performing an integrated circuit function; (4) cryogenic digital and linear applications; (5) novel types of solid-state devices (such as those utilizing the Hall effect, high-temperature circuit elements, and solid-state filters and delay lines); and (6) advanced circuitry, with emphasis on significant developments in the art, to the exclusion of data on equipment design. Papers representing original contributions in these and related fields are invited.

Abstracts (300-500 words) which can be accompanied by key illustrations, and 50-word summaries for advance program mailings, should be submitted in double-spaced typewritten form (in triplicate) by October 14, 1960, to the program chairman, Jerome J. Suran, Building 3, Room 115, General Electric Company, Electronics Park, Syracuse, N. Y., and should be accompanied by the author's name, affiliation, position title, business and home addresses (including telephone number), and a brief biographical sketch.

Additional information on the meeting can be obtained from the secretary of the conference committee, F. H. Blecher, Bell Telephone Laboratories, Murray Hill, N. J.